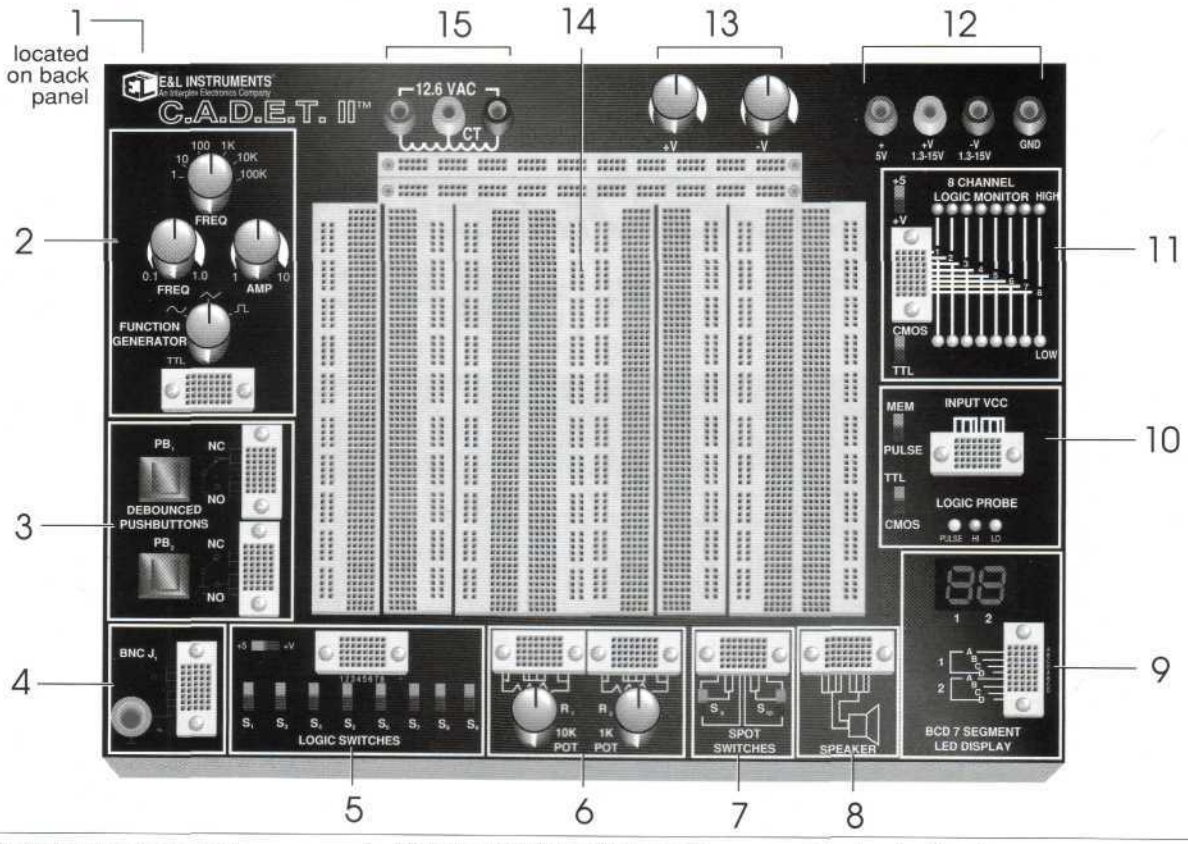

1 - Power Switch and Indicator
2 - Function Generator
3 - Debounced Pushbuttons
4 - BNC Connector
5 - Logic Switches
6 - 10K and 1K Potentiometers
7 - SPDT Switches
8 - Speaker
9 - BCD to 7 segment display
10 - Logic Probe
11 - Logic Monitor
12 - DC Power Supplies
13- Variable DC output controls
14 - Breadboard/Trainer Area
15 - AC Supply

SPECIFICATIONS:

POWER: 3-wire AC line input (117 V, 60 Hz typical) with power on indicator.

POWER SUPPLIES: Fixed DC output: +5V @ 1.0 A, ripple<5mV.
Variable DC output: +1.3V to +15V @ 0.5 A, ripple<5mV.
Variable DC output: -1.3V to -15V @ 0.5 A, ripple<5mV.
Fixed AC Output: 12.6VAC center tapped @100mA.

FUNCTION GENERATOR: Frequency range: 0.1 Hz to 100KHz in six ranges.
Output voltage: 0 to +/- 10 V (20 V p-p).
Output impedance: 600 ohms (except TTL).
Output current: 10 mA maximum, short circuit protected.
Output waveforms: sine, square, triangle, TTL.
Sine wave: distortion<3% (10 Hz to 100KHz).
TTL pulse: rise and fall time<25 nS, drive 10 TTL loads.
Square wave: rise and fall time, 1.5 µS.

LOGIC INDICATORS: 16 LEDs; 8 red to indicate logic high, 8 green to indicate a logic low.

DEBOUNCED PUSHBUTTONS (PULSERS): Two pushbutton-operated, open-collector output pulsers, each with one normally-open, one normally-closed output. Each output can sink up to 250 mA.

POTENTIOMETERS: One 1 K ohm, one 10 K ohm, all leads available and uncommitted.

SWITCHES: Two SPDT slide switches, all leads available and uncommitted. 8 Logic switches: one side of all eight switches connected and switchable to +5V or ground, other side of all eight switches separate, available, and uncommitted.

BNC CONNECTOR: One BNC connector, pin available and uncommitted, shell connected to ground.

SPEAKER: 0.25 W, 8 ohms.

BCD TO 7 Segment Drivers\Display: Two internal BCD to 7 segment display drivers and two 0.4" LEDs internally wired to a front panel tie block. BCD signal applied will display that value digit, in decimal form.

LOGIC PROBE: Built-in TTL/CMOS compatible logic probe. Captures fast single shot events and pulse trains. Input impedance 300KΩ, overload protection ±50VDC, min. detectable pulse width 100nsec. TTL Threshold low=0.8V high=2.25V, CMOS Threshold low=30%Vcc high=70%Vcc. Memory Mode, Latches on first transition and LED remains on until probe is reset.

BREADBOARDING AREA: A total of 3300 uncommitted tie-points available on velcro breadboard plate. Two QT-59B Bus strips. 50 tie-points each (for +5V, +15V, -15V and ground if desired.)

ENCLOSURE: High impact steel case.

WEIGHT: 10 lbs (4.5 kg)

DIMENSIONS: 4 1/2" H x 15" W x 10 3/4" D (11.4 x 38.1 x 27.3 cm)

ORDERING INFORMATION:
325-1450-Ruggedized C.A.D.E.T. II and one student socket plate.
325-1448-Student Socket Plate.
325-1449-C.A.D.E.T. II w/o breadboard plate.

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